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Contemporary Logic Design by Randy H. Katz

He is a Fellow of the ACM and the IEEE, and a member of the National Academy of Engineering and the American Academy of Arts and Sciences. He has published over 250 refereed technical papers, book chapters, and books. His textbook, Contemporary Logic Design, has sold over 85,000 copies, and has been used at over 200 colleges and universities. A second edition, co-written with Gaetano Borriello, was published in 2005.

Randy H. Katz | EECS at UC Berkeley

This is a sound, competent introduction to the elements of contemporary logic design. It covers all the basics: boolean logic, gate implementations, and the elements of binary arithmetic. It talks about minimization techniques, logic delays, and some of the classic logic implementation technologies (PLAs, decoders, muxes).

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Randy Katz is a Professor of Electrical Engineering and Computer Science at the University of California, Berkeley. He recently received a Distinguished Teaching Award for his contributions to engineering education, and he is coinvestigator on a multicampus NSF-sponsored project to restructure education.

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Randy Howard Katz is a distinguished professor at University of California, Berkeley of the electrical engineering and computer science department. Katz received an A.B. from Cornell University (May 1976), MS from UC Berkeley (June 1978), and Ph.D. from UC Berkeley (June 1980) all in computer science. Katz is a fellow of both the Association for Computing Machinery (ACM) and Institute of ...

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